



PTO/SB/51S (05-03)

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**SUPPLEMENTAL DECLARATION
FOR REISSUE
PATENT APPLICATION
TO CORRECT "ERRORS" STATEMENT
(37 CFR 1.175)**

Attorney Docket Number	501.34214R00
First Named Inventor	T. TAKAHASHI, et al.
COMPLETE	
Application Number	09/974,962
Filing Date	October 12, 2001
Art Unit	2824
Examiner Name	V. T. Nguyen

I/We hereby declare that:

Every error in the patent which was corrected in the present reissue application, and which is not covered by the prior oath(s) and/or declaration(s) submitted in this application, arose without any deceptive intention on the part of the applicant.

I/We hereby declare that all statements made herein of my/our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Name of Sole or First Inventor:☐ A petition has been filed for this unsigned inventor

Given Name (first and middle [if any])

Family Name or Surname

Tsugio

TAKAHASHI

Inventor's
Signature*Tsugio Takahashi*

Date

*4/30/2004***Name of Second Inventor:**☐ A petition has been filed for this unsigned inventor

Given Name (first and middle [if any])

Family Name or Surname

Goro

KITSUKAWA

Inventor's
Signature*Goro Kitsukawa*

Date

*4/12/2004***Name of Third Inventor:**☐ A petition has been filed for this unsigned inventor

Given Name (first and middle [if any])

Family Name or Surname

Takesada

AKIBA

Inventor's
Signature*Takesada Akiba*

Date

*4/7/2004***Name of Fourth Inventor:**☐ A petition has been filed for this unsigned inventor

Given Name (first and middle [if any])

Family Name or Surname

Yasushi

KAWASE

Inventor's
Signature*Yasushi Kawase*

Date

4/7/2004☒ Additional inventors or legal representatives(s) are being named on the 1 supplemental sheets PTO/SB/02A or 02LR attached hereto.

This collection of information is required by 37 CFR 1.175. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.8 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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DECLARATION**ADDITIONAL INVENTOR(S)**

Supplemental Sheet

Page 1 of 1

Name of Additional Joint Inventor, if any:		<input type="checkbox"/> A petition has been filed for this unsigned inventor	
Given Name (first and middle (if any))		Family Name or Surname	
Masayuki		NAKAMURA	
Inventor's Signature <i>Masayuki Nakamura</i>		Date <i>4/16/2009</i>	
Residence: City	State	Country	Citizenship
Mailing Address			
Mailing Address			
City	State	Zip	Country
Name of Additional Joint Inventor, if any:		<input type="checkbox"/> A petition has been filed for this unsigned inventor	
Given Name (first and middle (if any))		Family Name or Surname	
Inventor's Signature		Date	
Residence: City	State	Country	Citizenship
Mailing Address			
Mailing Address			
City	State	Zip	Country
Name of Additional Joint Inventor, if any:		<input type="checkbox"/> A petition has been filed for this unsigned inventor	
Given Name (first and middle (if any))		Family Name or Surname	
Inventor's Signature		Date	
Residence: City	State	Country	Citizenship
Mailing Address			
Mailing Address			
City	State	Zip	Country

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BOX IF
Expedited Procedure
Group No. 2824
501.34214R00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. TAKAHASHI, et al.
Application No.: 09/974,962
Filed: October 12, 2001
For: SEMICONDUCTOR MEMORY
Art Unit: 2824
Examiner: V. T. Nguyen

Reissue Patent Application Statement As To Loss Of Original Patent

Commissioner For Patents
P.O. Box 1450
Washington, D.C. 20231

Sir:

We, Tsugio TAKAHASHI, Goro KITSUKAWA, Takesada AKIBA, Yasushi KAWASE AND Masayuki NAKAMURA, hereby state that we are Applicants for a reissue patent based on the original Patent 5,966,341, entitled Semiconductor Memory, granted on October 12, 1999, with the reissue application being Serial No. 09/974,962. With regard to the original Patent 5,966,341, we hereby state that the ribboned original patent grant is lost or inaccessible.

Tsugio TAKAHASHI

Tsugio Takahashi
Inventor's Signature

7/02/2004
Date

Goro KITSUKAWA

Goro Kitsukawa
Inventor's Signature

6/23/2004
Date

Takesada AKIBA

Takesada Akiba
Inventor's Signature

6/14/2004
Date

Yasushi KAWASE

Yasushi Kawase
Inventor's Signature

6/14/2004
Date

Masayuki NAKAMURA

Masayuki Nakamura
Inventor's Signature

6/16/2004
Date